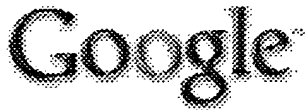


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Inherent HTTP Coherence

Cache can calculate **object hash** upon storage, or first need. ... does not address higher-level issues such as freshness calculation vs. **invalidation**, ...

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Re: MD5 hash query from Joe Cooper on 2001-12-28 (squid-dev)

requests to Squid to **invalidate** the **objects** that need to be invalidated. The reason I need the **hash** is because in order to provide "full tree" ...

www.squid-cache.org/mail-archive/squid-dev/200112/0150.html - 12k -

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The class **Cache** looks like a variant of **Hash**, and, in fact, **Cache** supports most of the methods of **Hash**. To control how to **invalidate** excessive **objects**, ...

www.nongnu.org/pupa/ruby-cache-MANUAL.html - 10k - [Cached](#) - [Similar pages](#)

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Objects in the **cache** are referenced via a **hash** table keyed ... archical **invalidation** schemes, but that sites exporting time- ...

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an **object** hits the **cache**. In server-side **invalidation**, ... **cache** server incorporates an **object cache**, which is implemented as a Java **Hash** Table data struc- ...

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2x) of retrieving from a **hash** table. Ideally it would be nice to be as ... Flushing the memory **cache** will also **invalidate** memory **objects** spooled to disk. ...

jcp.org/aboutJava/communityprocess/jsr/cacheFS.pdf - [Similar pages](#)

Core API Documentation: Class CacheTable

If the **hash** code sent by node X matches the **hash** code of the **object** mapped to the same key on node Y, then the **cache** entry maintained by Y need not be ...

byline.objectweb.org/ccm-core-6.1.0/api/com/arsdigita/caching/CacheTable.html - 28k -

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Object-Oriented DBMS

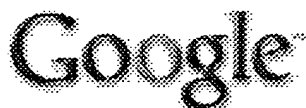
Goal: Allow clients to **cache** **objects**/pages and maybe locks from server for ... Mistaken **invalidation** is managed by keeping a LIFO list of **invalidate** page ...

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WebSphere Dynamic Cache: Improving J2EE application performance

A **cache** policy using the **hash** of the SOAP envelope could be defined as follows: ... Table 6 Performance measurements using **object cache and invalidation** ...

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Re: MD5 hash query from Joe Cooper on 2001-12-28 (squid-dev)clean why I need to be able to match the hash:. I'm making a utility that will provide **invalidation** services for Squid. The first is simply an indexer that ...www.squid-cache.org/mail-archive/squid-dev/200112/0150.html - 12k -[Cached](#) - [Similar pages](#)**Encrypted key cache** - Patent 6981138with the **hash** value as the **key**, or Eh(F)(F). Next, read access control ... For example, a **cache** entry **invalidation** command or message could be sent by some ...www.freepatentsonline.com/6981138.html - 83k - [Cached](#) - [Similar pages](#)**MANUAL.rd**The class **Cache** looks like a variant of **Hash**, and, in fact, **Cache** supports most of the ...Return keys and cached objects as a **Hash** object. **invalidate(key ...**www.nongnu.org/pupa/ruby-cache-MANUAL.html - 10k - [Cached](#) - [Similar pages](#)**'RE: External/Event Based Cache Invalidation (somewhat long)' - MARC**When the **key** is ejected from the **cache** for other > reasons (another > > > pipeline ... because of > > grouping of multiple keys under the same **hash** code. ...marc2.theaimsgroup.com/?l=xm1-cocoon-dev&m=105698867207856&w=2 - [Similar pages](#)**'RE: External/Event Based Cache Invalidation (somewhat long)' - MARC**When the **key** is ejected from the **cache** for other reasons (another > pipeline ... In order for this to work, all keys must return the exact same **hash** code. ...marc2.theaimsgroup.com/?l=xm1-cocoon-dev&m=105688316717880&w=2 - [Similar pages](#)**Controlling Cache Behaviour for Portlet Markup Fragments in ...**Enhanced **cache** key Enhanced **cache** key. The state **hash** can be used as a shorthand ...Finally, **cache** **invalidation** can also be done programmatically. ...www.ibm.com/developerworks/websphere/library/techarticles/0401_voth/0401_voth.html -67k - [Cached](#) - [Similar pages](#)**[ps] Tasks and Revocation for Java (or, Hey! You got your Operating ...**File Format: Adobe PostScript - [View as HTML](#)For instance, a **hash** table object expecting keys of type " ... Luckily, standard **caching** and **invalidation** techniques apply to remote pointer accesses, ...www.cs.cornell.edu/Info/People/hawblitz/PLDI2000-submit/luna-99-11-13.ps - [Similar pages](#)**Distributed Caching with Memcached | Linux Journal**A **hash** function is a one-way function mapping a **key** (be it numeric or ... This is unfortunate when the write is an important **cache** **invalidation** operation. ...www.linuxjournal.com/article/7451 - 54k - Aug 1, 2006 - [Cached](#) - [Similar pages](#)**Core API Documentation: Class CacheTable**If the **hash** code sent by node X matches the **hash** code of the object mapped to the same **key** on node Y, then the **cache** entry maintained by Y need not be ...byline.objectweb.org/ccm-core-6.1.0/api/com/arsdigita/caching/CacheTable.html - 28k -

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of Systems while(continue cond) f x =**hash**[index1]**hash**[index2] y g Epoch 1
Extending **Cache** Coherence to Support Thread-Level Data
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1 [Client-side caching for TLS](#)



Hovav Shacham, Dan Boneh, Eric Rescorla

 November 2004 **ACM Transactions on Information and System Security (TISSEC)**,

Volume 7 Issue 4

Publisher: ACM Press

 Full text available: [pdf\(182.01 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We propose two new mechanisms for caching handshake information on TLS clients. The "fast-track" mechanism provides a client-side cache of a server's public parameters and negotiated parameters in the course of an initial, enabling handshake. These parameters need not be resent on subsequent handshakes. Fast-track reduces both network traffic and the number of round trips, and requires no additional server state. These savings are most useful in high-latency environments such as wireless network ...

Keywords: Bloom filters, TLS, session cache, wireless networks

2 [Session 8: distributed systems: Transparent caching with strong consistency in dynamic content web sites](#)



Cristiana Amza, Gokul Soundararajan, Emmanuel Cecchet

 June 2005 **Proceedings of the 19th annual international conference on Supercomputing ICS '05**

Publisher: ACM Press

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3 [Energy-efficient selective cache invalidation](#)

Jun Cai, Kian-Lee Tan

 December 1999 **Wireless Networks**, Volume 5 Issue 6

Publisher: Kluwer Academic Publishers

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